



## Inventor Name Search Result

Your Search was:

Last Name = PAI

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Application#	Patent#	PG Pub#	Status	Date Filed	Title	Examiner Name	Inventor I
<a href="#">11092932</a>	Not Issued	20060227876	61	03/29/2005	System, method, and apparatus for AC coefficient prediction	TORRENTE,RICHARD	PAI, RAM L.
<a href="#">10735980</a>	<a href="#">7284072</a>	20050038938	150	12/15/2003	DMA ENGINE FOR FETCHING WORDS IN REVERSE ORDER	FRANKLIN,RICHARD	PAI, RAM LAKSHM
<a href="#">10736125</a>	Not Issued	20050036614	41	12/15/2003	DMA engine for fetching words in reverse bit order	TSENG,CHENG YUAN	PAI, RAM LAKSHM
<a href="#">10775652</a>	Not Issued	20050175106	71	02/09/2004	Unified decoder architecture	RASHIDIAN,MOHAMMAD	PAI, RAM LAKSHM
<a href="#">10816118</a>	Not Issued	20050169375	71	04/01/2004	Read/write separation in video request manager	HOLDER,ANNER	PAI, RAM LAKSHM
<a href="#">10816320</a>	Not Issued	20050169376	71	04/01/2004	Motion vector address computer error detection	HOLDER,ANNER	PAI, RAM LAKSHM
<a href="#">10824897</a>	Not Issued	20050232355	61	04/15/2004	Video decoder for supporting both single and four motion	ROBERTS,JESSICA	PAI, RAM LAKSHM

					vector macroblocks		
<a href="#">11096653</a>	Not Issued	20060224390	41	04/01/2005	System, method, and apparatus for audio decoding accelerator	RIDER,JUSTIN	PAI, RAM LAKSHM
<a href="#">60494666</a>	Not Issued		159	08/13/2003	DMA state machine for fetching addresses in reverse order	,	PAI, RAM LAKSHM
<a href="#">60494746</a>	Not Issued		159	08/13/2003	DMA architecture for providing data words in reverse order	,	PAI, RAM LAKSHM

**Inventor Search Completed: No Records to Display.**

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